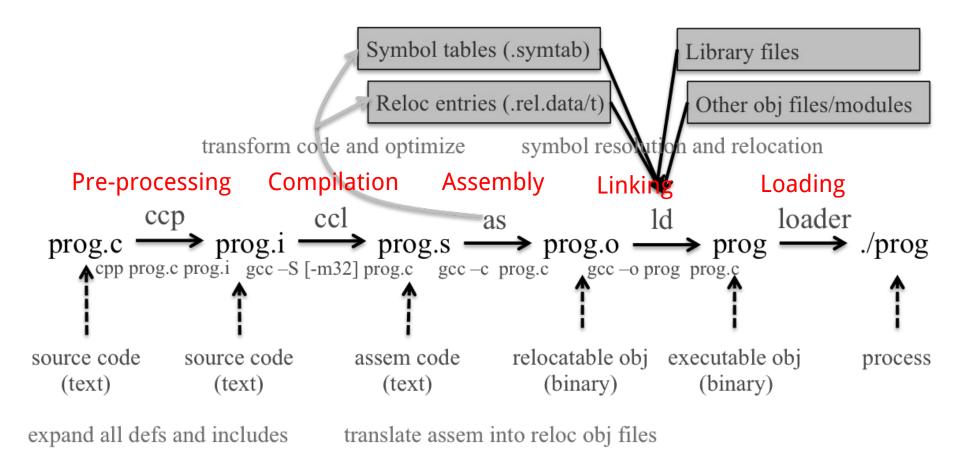
## **CSE 410/565: Computer Security**

Instructor: Dr. Ziming Zhao

# Background Knowledge: Compiler, linker, and loader

## From a C program to a process



## Loading and Executing a Binary Program on Linux

Validation (permissions, memory requirements etc.)

Operating system starts by setting up a new process for the program to run in, including a virtual address space.

The operating system maps an interpreter into the process's virtual memory.

## Interpreter, e.g., /lib/ld-linux.so in Linux

The interpreter loads the binary into its virtual address space (the same space in which the interpreter is loaded).

It then parses the binary to find out (among other things) which dynamic libraries the binary uses.

The interpreter maps these into the virtual address space (using *mmap* or an equivalent function) and then performs any necessary last-minute relocations in the binary's code sections to fill in the correct addresses for references to the dynamic libraries.

## Compiling a C program behind the scene (add\_32 add\_64)

add.o	C	add.h	main.c							
#include "a	dd.h"	#ifndef ADD_H #define ADD_H	/* This program has an integer overflow vulnerability. */ #include "add.h" #include <stdio.h></stdio.h>							
#define BAS	SE 50	int add(int, int);	<pre>#include <string.h> #include <stdlib.h> #define USAGE "Add two integers with 50. Usage: add a b\n"</stdlib.h></string.h></pre>							
int add(int a { return a + BASE;}		#endif	<pre>int main(int argc, char *argv[]) {     int a = 0;     int b = 0;</pre>							
gcc -Wall -save-ter	nps -P -m32 -O2	add.c main.c -o add_32	if (argc != 3) { printf(USAGE); return 0;}							
gcc -Wall -save-ter	nps -P -O2 add.c	main.c -o add_64	a = atoi(argv[1]); b = atoi(argv[2]); printf("%d + %d + 50 = %d\n", a, b, add(a, b)); }							

# Background Knowledge: x86 architecture

## Data Types

There are 5 integer data types:

Byte – 8 bits. Word – 16 bits. Dword, Doubleword – 32 bits. Quadword – 64 bits. Double quadword – 128 bits.

## Endianness

Little Endian (Intel, ARM)
 Least significant byte has lowest address
 Dword address: 0x0
 Value: 0x78563412

Big Endian
 Least significant byte has highest address
 Dword address: 0x0
 Value: 0x12345678

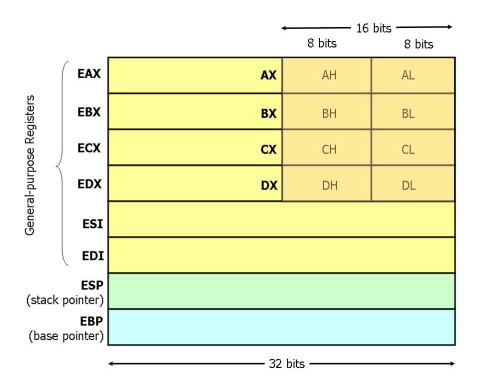
Address 0	0x12
Address 1	0x34
Address 2	0x56
Address 3	0x78

## **Base Registers**

There are

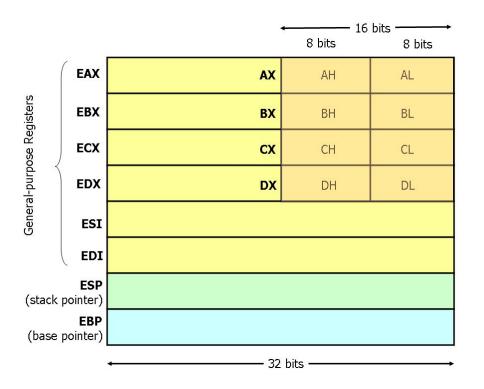
- Eight 32-bit "general-purpose" registers,
- One 32-bit EFLAGS register,
- One 32-bit instruction pointer register (eip), and
- Other special-purpose registers.

## **The General-Purpose Registers**



- 8 general-purpose registers
- esp is the stack pointer
- ebp is the base pointer
- esi and edi are source and destination index registers for array and string operations

## **The General-Purpose Registers**



- The registers eax, ebx, ecx, and edx may be accessed as 32-bit, 16-bit, or 8-bit registers.
- The other four registers can be accessed as 32-bit or 16-bit.

## **EFLAGS Register**

The various bits of the 32-bit EFLAGS register are set (1) or reset/clear (0) according to the results of certain operations.

We will be interested in, at most, the bits

```
CF – carry flag
PF – parity flag
ZF – zero flag
SF – sign flag
```

## **Instruction Pointer (EIP)**

Finally, there is the EIP register, which is the instruction pointer (program counter). Register EIP holds the address of the **next** instruction to be executed.

## **Registers on x86 and amd64**

ZMM0	YMM0 XMM0	ZMM1	YMM1 XM	M1	ST(0)	MM0	ST(1)	MM1	ALAHAXE	AX RAX	R8B R8W R8D	R8 R12BR12V	W R12D R12	MSWC	R0 CR	4
ZMM2	YMM2 XMM2	ZMM3	YMM3 XM	M3	ST(2)	MM2	ST(3)	MM3	вцвнВХЕ	BXRBX	R9B R9W R9D	R9 R138R13V	W R13D R13	CRI	L CR	5
ZMM4	YMM4 XMM4	ZMM5	YMM5 XM	M5	ST(4)	MM4	ST(5)	MM5	СССНСХЕ		R10BR10W R10D	R10 R14BR14V	W R14D R14	CR2	2 CR	6
ZMM6	YMM6 XMM6	ZMM7	YMM7 XM	M7	ST(6)	MM6	ST(7)	MM7			R11BR11W R11D	R11 R158R15V	W R15D R15	CRE	3 CR	7
ZMM8	YMM8 XMM8	ZMM9	YMM9 XM	M9					BPLBPEB	PRBP		DI IP	EIP RIP	MXCS	SR CR	8
ZMM10	YMM10 XMM10	ZMM11	YMM11 XM	M11	CW	FP_IP	FP_DP	FP_CS	SIL SI ES	I RSI	SPL SP ESP R	SP			CR	9
ZMM12	YMM12 XMM12	ZMM13	YMM13 XM	M13	SW										CRI	LO
ZMM14	YMM14 XMM14	ZMM15	YMM15 XM	M15	TW		8-bit re			register		egister		register	CRI	11
ZMM16 ZMM	M17 ZMM18 ZMM19	ZMM20 ZM	1M21 ZMM22 Z	MM23	FP_DS		16-bit	register	04-bit	register	120-010	register	512-bit	register	CRI	12
ZMM24 ZMM	M25 ZMM26 ZMM27	ZMM28 ZM	1M29 ZMM30 Z	MM31	FP_OPC	FP_DP	FP_IP	CS	SS	DS	GDTR	IDTR	DR0	DR6	CRI	13
								ES	FS	GS	TR	LDTR	DR1	DR7	CRI	14
											FLAGS EFLAGS	RELAGS	DR2	DR8	CRI	15
													DR3	DR9		
													DR4	DR10	DR12	DR14
													DR5	DR11	DR13	DR15

## Instructions

Each instruction is of the form

label: mnemonic operand1, operand2, operand3 The label is optional.

The number of operands is 0, 1, 2, or 3, depending on the mnemonic .

Each operand is either

- An immediate value,
- A register, or
- A memory address.

## **Source and Destination Operands**

Each operand is either a source operand or a destination operand.

A source operand, in general, may be

- An immediate value,
- A register, or
- A memory address.

A destination operand, in general, may be

- A register, or
- A memory address.

## Instructions

**hlt** – 0 operands halts the central processing unit (CPU) until the next external interrupt is fired

inc - 1 operand; inc <reg>, inc <mem>

add - 2 operands; add <reg>,<reg>

imul – 1, 2, or 3 operands; imul <reg32>,<reg32>,<con>

## **Intel Syntax Assembly and Disassembly**

Machine instructions generally fall into three categories: data movement, arithmetic/logic, and control-flow.

<reg32> Any 32-bit register (eax, ebx, ecx, edx, esi, edi, esp, or ebp) <reg16> Any 16-bit register (ax, bx, cx, or dx) <reg8> Any 8-bit register (ah, bh, ch, dh, al, bl, cl, or dl) <reg> Any register <mem> A memory address (e.g., [eax] or [eax + ebx\*4]); [] square brackets <con32> Any 32-bit immediate <con16> Any 16-bit immediate <con8> Any 8-bit immediate <con> Any 8-, 16-, or 32-bit immediate

## **Addressing Memory**

Move from source (operand 2) to destination (operand 1)

**mov [eax], ebx** (read as MOVE FROM x to y) Load 4 bytes from the memory address in EBX into EAX.

mov eax, [esi - 4] Move 4 bytes at memory address ESI - 4 into EAX. \*/

**mov [esi + eax \* 1], cl** Move the contents of CL into the byte at address ESI+EAX\*1.

**mov edx, [esi + ebx\*4]** Move the 4 bytes of data at address ESI+4\*EBX into EDX.

## **Addressing Memory**

The size directives BYTE PTR, WORD PTR, and DWORD PTR serve this purpose, indicating sizes of 1, 2, and 4 bytes respectively.

mov [ebx], 2 isn't this ambiguous? We can have a default.

**mov BYTE PTR [ebx], 2** Move 2 into the single byte at the address stored in EBX.

**mov WORD PTR [ebx], 2** Move the 16-bit integer representation of 2 into the 2 bytes starting at the address in EBX.

**mov DWORD PTR [ebx], 2** Move the 32-bit integer representation of 2 into the 4 bytes starting at the address in EBX.

### **Data Movement Instructions**

mov — Move

Syntax mov <reg>, <reg> mov <reg>, <mem> mov <mem>, <reg> mov <reg>, <con> mov <mem>, <con>

Examples mov eax, ebx — copy the value in EBX into EAX mov byte ptr [var], 5 — store the value 5 into the byte at location var

### **Data Movement Instructions**

**push** — Push on stack; decrements ESP by 4, then places the operand at the location ESP points to.

Syntax push <reg32> push <mem> push <con32>

Examples push eax — push eax on the stack push [var] — push the 4 bytes at address var onto the stack

### **Data Movement Instructions**

**pop** — Pop from stack

Syntax pop <reg32> pop <mem>

Examples pop edi — pop the top element of the stack into EDI. pop [ebx] — pop the top element of the stack into memory at the four bytes starting at location EBX.

## **LEA Instructions**

lea — Load effective address; used for quick calculation

Syntax lea <reg32>, <mem>

Examples Lea edi, [ebx+4\*esi] — the quantity EBX+8\*ESI is placed in EDI.

## **Arithmetic and Logic Instructions**

**add** eax, 10 — EAX is set to EAX + 10 **addb** byte ptr [eax], 10 — add 10 to the single byte stored at memory address stored in EAX

sub al, ah — AL is set to AL - AHsub eax, 216 — subtract 216 from the value stored in EAX

**dec** eax — subtract one from the contents of EAX

**imul** eax, [ebx] — multiply the contents of EAX by the 32-bit contents of the memory at location EBX. Store the result in EAX.

**shr** ebx, cl — Store in EBX the floor of result of dividing the value of EBX by 2n where n is the value in CL.

**jmp** — Jump

Transfers program control flow to the instruction at the memory location indicated by the operand.

Syntax jmp <label> # direct jump jmp <reg32> # indirect jump

Example jmp begin — Jump to the instruction labeled begin.

#### jcondition — Conditional jump

Syntax je <label> (jump when equal) jne <label> (jump when not equal) jz <label> (jump when last result was zero) jg <label> (jump when greater than) jge <label> (jump when greater than or equal to) jl <label> (jump when less than) jle <label> (jump when less than or equal to)

Example

cmp ebx, eax jle done

**cmp** — Compare

```
Syntax
cmp <reg>, <reg>
cmp <mem>, <reg>
cmp <reg>, <mem>
cmp <con>, <reg>
```

Example cmp byte ptr [ebx], 10 jeq loop

If the byte stored at the memory location in EBX is equal to the integer constant 10, jump to the location labeled loop.

#### **call** — Subroutine call

The call instruction first **pushes the current code location onto the hardware supported stack** in memory, and then performs **an unconditional jump to the code** location indicated by the label operand. Unlike the simple jump instructions, the call instruction saves the location to return to when the subroutine completes.

Syntax call <label> call <reg32> Call <mem>

#### **ret** — Subroutine return

The ret instruction implements a subroutine return mechanism. This instruction pops a code location off the hardware supported in-memory stack to the program counter.

Syntax ret

## The Run-time Stack

The run-time stack supports procedure calls and the passing of parameters between procedures.

The stack is located in memory.

The stack grows towards **low memory**.

When we push a value, esp is decremented.

When we pop a value, esp is incremented.

## **Stack Instructions**

enter — Create a function frame

Equivalent to:

push ebp mov ebp, esp sub esp, Imm

## **Stack Instructions**

**leave** — Releases the function frame set up by an earlier ENTER instruction.

Equivalent to:

mov esp, ebp pop ebp

# Background Knowledge: amd64 architecture

## **Registers on x86 and x86-64**

ZMM0	YMM0	XMM0	ZMM1	Y	MM1	XMM1	ST(0)	MM0	ST(1)	MM1		нахел	AX RAX	R8B R8W R8D	R8 R128R12V	W R12D R12	MSWC	R0 CR	4
ZMM2	YMM2	XMM2	ZMM3	Y	ИМ3	XMM3	ST(2)	MM2	ST(3)	MM3	BLB	н <mark>ВХ</mark> ЕІ	BXRBX	R9B R9W R9D	R9 R138R13V	N R13D R13	CRI	L CR	5
ZMM4	YMM4	XMM4	ZMM5	Y	MM5	XMM5	ST(4)	MM4	ST(5)	MM5	СГС	нСХЕ	X RCX	R10BR10W R10D	R10 R14BR14V	N R14D R14	CR2	2 CR	6
ZMM6	YMM6	XMM6	ZMM7	Y	MM7	XMM7	ST(6)	MM6	ST(7)	MM7		HDXEI		R11BR11W R11D		N R15D R15	CRE	3 CR	7
ZMM8	YMM8	XMM8	ZMM9	Y	MM9	XMM9					BPL	BPEBI	RBP		DI	EIP RIP	MXCS	SR CR	8
ZMM10	YMM10	XMM10	ZMM1	1 Y	MM11	XMM11	CW	FP_IP	FP_DP	FP_CS	SIL	SI ES	I RSI		SP			CR	9
ZMM12	YMM12	XMM12	ZMM1	3 Y	MM13	XMM13	SW	]										CRI	LO
ZMM14	YMM14	XMM14	ZMM1	5 Y	MM15	XMM15	ΤW		8-bit n				register		register	256-bit	2	CRI	11
ZMM16 ZMI	M17 ZMM1	18 ZMM19	ZMM20	ZMM21	ZMM22	2 ZMM23	FP_DS		16-bit	register		04-DIU	register	120-DI	register	512-DIL	register	CRI	12
ZMM24 ZMI	M25 ZMM2	26 ZMM27	ZMM28	ZMM29	ZMM3	ZMM31	FP_OPC	FP_DP	FP_IP		s	SS	DS	GDTR	IDTR	DR0	DR6	CRI	13
										E	S	FS	GS	TR	LDTR	DR1	DR7	CR1	14
														FLAGS EFLAGS	RELAGS	DR2	DR8	CR1	15
																DR3	DR9		
																DR4	DR10	DR12	DR14
																DR5	DR11	DR13	DR15

# x86 vs. x86-64 (code/ladd)

ma	iin.c
<pre>/* This program has an integer overflow vulnerability.  */ #include <stdio.h> #include <string.h> #include <string.h> long long ladd(long long *xp, long long y) {     long long t = *xp + y;     return t; }</string.h></string.h></stdio.h></pre>	<pre>int main(int argc, char *argv[]) {     long long a = 0;     long long b = 0;     if (argc != 3)         {         printf("Usage: ladd a b\n");         return 0;         }         printf("The sizeof(long long) is %d\n", sizeof(long long));         a = atoll(argv[1]);         b = atoll(argv[2]);</pre>
gcc -Wall -m32 -O2 main.c -o ladd	printf("%lld + %lld = %lld\n", a, b, ladd(&a, b)); }
gcc -Wall -O2 main.c -o ladd64	

.

## x86 vs. x86-64 (code/ladd)

x86

000012c0	<ladd>:</ladd>	
12c0:	f3 0f 1e fb	endbr32
12c4:	8b 44 24 04	mov eax,DWORD PTR [esp+0x4]
12c8:	8b 50 04	mov edx,DWORD PTR [eax+0x4]
12cb:	8b 00	mov eax,DWORD PTR [eax]
12cd:	03 44 24 08	add eax,DWORD PTR [esp+0x8]
12d1:	13 54 24 0c	adc edx,DWORD PTR [esp+0xc]
12d5:	c3	ret

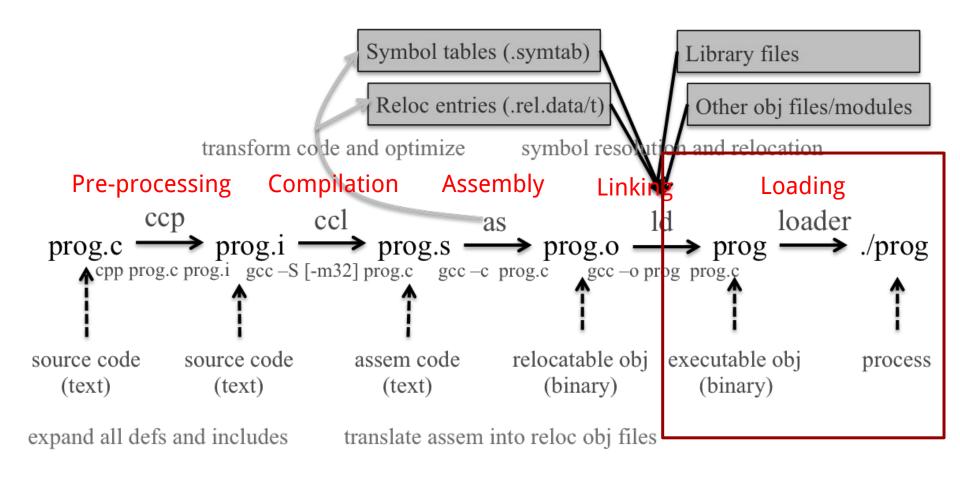
#### x86-64

000000000	)0001220 <lac< th=""><th>dd&gt;:</th><th></th></lac<>	dd>:	
1220:	f3 0f 1e fa	endbr64	
1224:	48 8b 07	mov rax,QWORD PTR [rdi]	
1227:	48 01 f0	add rax,rsi	
122a:	c3	ret	

objdump -M intel -d ladd\_32 objdump -M intel -d ladd\_64

# Background Knowledge: Set-UID Programs

### From a C program to a process



#### Real UID, Effective UID, and Saved UID

Each Linux/Unix **process** has 3 UIDs associated with it.

**Real UID (RUID)**: This is the UID of the user/process that created THIS process. It can be changed only if the running process has EUID=0.

**Effective UID (EUID)**: This UID is used to evaluate privileges of the process to perform a particular action. EUID can be changed either to RUID, or SUID if EUID!=0. If EUID=0, it can be changed to anything.

**Saved UID (SUID)**: If the binary image file, that was launched has a Set-UID bit on, SUID will be the UID of the owner of the file. Otherwise, SUID will be the RUID.

### **Set-UID Program**

The kernel makes the decision whether a process has the privilege by looking on the **EUID** of the process.

For non Set-UID programs, the effective uid and the real uid are the same. For Set-UID programs, **the effective uid is the owner of the program**, while the real uid is the user of the program.

What will happen is when a setuid binary executes, the process changes its Effective User ID (EUID) from the default RUID to the owner of this special binary executable file which in this case is - root.

	-	Think	Deda	¢ 1 c	1 /6-	-										
ziming@zimin total 12676	ıg-	Inthi	(Pad:-	•\$ LS -a	ι /Β	uny										
drwxr-xr-x	2	root	root	4096	May	26	00:14									
drwxr-xr-x 2							09:57									
-rwxr-xr-x																
-rwxr-xr-x								brltty								
-rwxr-xr-x	3	root	root	34888	Jul	4	2019	bunzip2								
-rwxr-xr-x	1	root	root	2062296	Маг	6	2019	busybox								
- FWXF-XF-X	3	root	root	34888				bzcat								
lrwxrwxrwx					Jul			bzcmp ->	bzdiff							
-rwxr-xr-x				2140			2019	bzdiff	EUVE VE V	1 500	+ 500	+ 2010		22	2010	cotuncon
lrwxrwxrwx					Jul				-rwxr-xr-x							setupcon
-rwxr-xr-x				4877				bzexe	lrwxrwxrwx				1 - 1 - 1 - <b>-</b> -			sh -> dash
lrwxrwxrwx					Jul				lrwxrwxrwx							sh.distrib -> dash
- FWXF - XF - X				3642				bzgrep	-rwxr-xr-x				) Jan			
- FWXF - XF - X				34888 14328				bzip2	-rwxr-xr-x	1 roo	t roo	t 139904			10:40	
-rwxr-xr-x lrwxrwxrwx					Jul		2019	bzless ->	lrwxrwxrwx	1 roo	t roo	t				static-sh -> busybox
- FWXF - XF - X				1297				bzmore	-rwxr-xr-x				2 Jan	18	2018	stty
- FWXF - XF - X				35064			2018		-rwsr-xr-x	1 roo	t roo	t 44664	1 Mar	22	2019	su
- FWXF - XF - X				14328				chacl	- FWXF-XF-X	1 roo	t roo	t 35000	) Jan	18	2018	sync
-rwxr-xr-x								charp	-rwxr-xr-x	1 roo	t roo	t 18235	2 May	3	07:30	systemctl
-rwxr-xr-x				59608				chmod	lrwxrwxrwx	1 гоо	t roo	t 20	May	3	07:30	<pre>systemd -&gt; /lib/systemd/systemd</pre>
-rwxr-xr-x	1	root	root	67768	Jan	18	2018	chown	-rwxr-xr-x	1 гоо	t roo	t 10320	May	3	07:30	systemd-ask-password
-rwxr-xr-x	1	root	root	10312	Jan	22	2018	chvt	-rwxr-xr-x				May	3	07:30	systemd-escape
-rwxr-xr-x	1	root	root	141528	Jan	18	2018	ср	-rwxr-xr-x	1 гоо	t roo	t 84328	3 Mav	3	07:30	systemd-hwdb
-rwxr-xr-x	1	root	root	157224	Nov	5	2019	cpio	-rwxr-xr-x							systemd-inhibit
-rwxr-xr-x	1	root	root					dash	-rwxr-xr-x							systemd-machine-id-setup
-rwxr-xr-x								date	-rwxr-xr-x							systemd-notify
- FWXF - XF - X				76000			2018		-rwxr-xr-x							systemd-sysusers
- rwxr - xr - x				84776			2018		-rwxr-xr-x							systemd-tmpfiles
- FWXF - XF - X				133792 72000			2018		- rwxr - xr - x							systemd-tty-ask-password-agent
-rwxr-xr-x	Τ.	1001	1001	72000	rial	Э	12.25	unesg		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1					2019	
									-rwxr-xr-x							tempfile
									-rwxr-xr-x						2018	
									-rwxr-xr-x				1 Jan		2018	
									-rwxr-xr-x							udevadm
									-rwxr-xr-x				_			ulockmgr_server
									-rwsr-xr-x							umount
									- rwxr - xr - x	1 roo	t roo	t 35032	2 Jan	18	2018	uname

-rwxr-xr-x	1	root	root	39103	Арг	23	2019	setupcon
lrwxrwxrwx	1	root	root					sh -> dash
lrwxrwxrwx	1	root	root	4	Aug	16	2018	sh.distrib -> dash
-rwxr-xr-x	1	root	root	35000	Jan	18	2018	sleep
-rwxr-xr-x	1	root	root	139904				
lrwxrwxrwx	1	root	root	7	Mar	6	2019	<pre>static-sh -&gt; busybox</pre>
-rwxr-xr-x	1	root	root	75992	Jan	18	2018	stty
-rwsr-xr-x	1	root	root	44664	Маг	22	2019	su
-rwxr-xr-x	1	root	root	35000	Jan	18	2018	sync
-rwxr-xr-x	1	root	root	182352	May	3	07:30	systemctl
lrwxrwxrwx	1	root	root	20	May	3	07:30	<pre>systemd -&gt; /lib/systemd/systemd</pre>
-rwxr-xr-x	1	root	root	10320	May	3	07:30	systemd-ask-password
-rwxr-xr-x	1	root	root	14400	May	3	07:30	systemd-escape
-rwxr-xr-x	1	root	root	84328	May	3	07:30	systemd-hwdb
-rwxr-xr-x	1	root	root	14416	May	3	07:30	systemd-inhibit
-rwxr-xr-x	1	root	root	18496	May	3	07:30	systemd-machine-id-setup
-rwxr-xr-x	1	root	root	14408	May	3	07:30	systemd-notify
-rwxr-xr-x	1	root	root	43080	May	3	07:30	systemd-sysusers
-rwxr-xr-x	1	root	root	71752	May	3	07:30	systemd-tmpfiles
-rwxr-xr-x	1	root	root	26696	May	3	07:30	systemd-tty-ask-password-agent
-rwxr-xr-x	1	root	root	423312	Jan	21	2019	tar
-rwxr-xr-x	1	root	root	10104	Dec	30	2017	tempfile
-rwxr-xr-x	1	root	root	88280	Jan	18	2018	touch
-rwxr-xr-x	1	root	root	30904	Jan	18	2018	true
-rwxr-xr-x	1	root	root	584072	May	3	07:30	udevadm
-rwxr-xr-x	1	root	root	14328	Aug	11	2016	ulockmgr_server
-rwsr-xr-x	1	root	root	26696	Mar	5	12:23	umount
-rwxr-xr-x	1	root	root	35032	Jan	18	2018	uname

## **Example: rdsecret**

```
main.c
#include <stdio.h>
                                                                     if (pw)
#include <string.h>
#include <stdlib.h>
                                                                           printf("EUID: %d, EUSER: %s.\n", euid, pw->pw name);
#include <unistd.h>
#include <sys/types.h>
#include <pwd.h>
                                                                      print_flag();
int main(int argc, char *argv[])
                                                                      return(0);
 FILE *fp = NULL;
 char buffer[100] = \{0\};
                                                                    void print_flag()
 // get ruid and euid
 uid t uid = getuid();
                                                                           FILE *fp;
 struct passwd *pw = getpwuid(uid);
                                                                           char buff[MAX_FLAG_SIZE];
                                                                           fp = fopen("flag","r");
 if (pw)
                                                                           fread(buff, MAX_FLAG_SIZE, 1, fp);
       printf("UID: %d, USER: %s.\n", uid, pw->pw name);
                                                                           printf("flag is : %s\n", buff);
                                                                           fclose(fp);
 uid t euid = geteuid();
 pw = getpwuid(euid);
```

# Background Knowledge: ELF Binary Files

# **ELF Files**

The **Executable** and **Linkable Format** (**ELF**) is a common standard file format for *executable files*, *object code*, *shared libraries*, and *core dumps*. Filename extension *none*, *.axf*, *.bin*, *.elf*, *.o*, *.prx*, *.puff*, *.ko*, *.mod* and *.so* 

Contains the program and its data. Describes how the program should be loaded (program/segment headers). Contains metadata describing program components (section headers).

#### Command file

ziming@ziming-XPS-13-9300:-\$ file /bin/ls
/bin/ls: ELF 64-bit LSB shared object, x86-64, version 1 (SYSV), dynamically lin
ked, interpreter /lib64/ld-linux-x86-64.so.2, BuildID[sha1]=2f15ad836be3339dec0e
2e6a3c637e08e48aacbd, for GNU/Linux 3.2.0, stripped
ziming@ziming-XPS-13-9300:-\$

<i>,</i>	· · · · · · · · · .
	•
:file /bin/ls	
	•
•	

ziming@ziming-XPS-13-9300	:-S readelf	-a /h	pin/ls										
ELF Header:	. y reddeen	0 /0	, city es										
	01 01 00 00	00 0	00 00 00 00 00 00										
Class:		ELF64											
Data:		2's complement, little endian											
Version:		1 (current)											
OS/ABI:			- System V										
ABI Version:		0	System v										
Type:			Shared object fil	a)									
Machine:			nced Micro Devices										
Version:		0x1	iced filtero bevilles	X00-04									
Entry point address: 0x1													
Start of section headers: 140224 (bytes into file) Flags: 0x0													
Size of this header:			ovtes)										
Size of program headers			oytes)										
Number of program headers		13	Jyces)										
Size of section headers			ovtes)										
Number of section headers		30	Jyces)										
Section header string t		29											
section header string i	able index:	29											
Section Headers:													
[Nr] Name	Tupo		Address	Offset									
	Type												
Size	EntSize NULL		Flags Link Info Align 000000000000000 0000000										
[ 0]		0000		00000000									
00000000000000000	00000000000000	0000	0 0										
[ 1] .interp	PROGBITS 00000000000000	0000	000000000000318 A 0 0	00000318									
000000000000001c		0000											
[ 2] .note.gnu.propert		0000	00000000000338	00000338									
0000000000000020	0000000000000	0000	A 0 0	8									
[ 3] .note.gnu.build-i		0000	000000000000358	00000358									
0000000000000024	0000000000000	0000	A 0 0										
[ 4] .note.ABI-tag	NOTE	0000	00000000000037c	0000037c									
0000000000000020	00000000000000	0000	A 0 0	4									
[ 5] .gnu.hash	GNU_HASH	0000	0000000000003a0	000003a0									
00000000000000e4	00000000000000	0000	A 6 0	8									
[ 6] .dynsym	DYNSYM	0040	000000000000488	00000488									
00000000000000000000000000000000000000	00000000000000	0018	A 7 1										
[7].dynstr	STRTAB	0000	000000000001190	00001190									
00000000000064c	000000000000000000000000000000000000000	0000	A 0 0	1									
[ 8] .gnu.version	VERSYM		00000000000017dc	000017dc									
000000000000116	000000000000000000000000000000000000000	0002	A 6 0										
[ 9] .gnu.version_r	VERNEED		00000000000018f8	000018f8									
0000000000000070	000000000000000	0000	A 7 1	8									
[10] .rela.dyn	RELA	0040	000000000001968	00001968									
000000000001350	000000000000000	0018	A 6 0										
[11] .rela.plt	RELA		0000000000002cb8										
00000000000009f0	00000000000000	0018	AI 6 25										
[12] .init	PROGBITS	1111	0000000000004000	00004000									
00000000000001b	00000000000000	0000	AX 0 0										
[13] .plt	PROGBITS		0000000000004020	00004020									
aaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaa	000000000000000000000000000000000000000		AV A A	16									

00000000000006b0 00000000000000000 AX

**INTERP:** defines the library that should be used to load this ELF into memory. **LOAD:** defines a part of the file that should be loaded into memory.

#### Sections:

.text: the executable code of your program. .plt and .got: used to resolve and dispatch library calls.

.data: used for pre-initialized global writable data (such as global arrays with initial values) .rodata: used for global read-only data (such as string constants)

**.bss:** used for uninitialized global writable data (such as global arrays without initial values)

# **Tools for ELF**

gcc to make your ELF.
readelf to parse the ELF header.
objdump to parse the ELF header and disassemble the source code.
nm to view your ELF's symbols.
patchelf to change some ELF properties.
objcopy to swap out ELF sections.
strip to remove otherwise-helpful information (such as symbols).
kaitai struct (https://ide.kaitai.io/) to look through your ELF interactively.

# Background Knowledge: Memory Map of a Linux Process

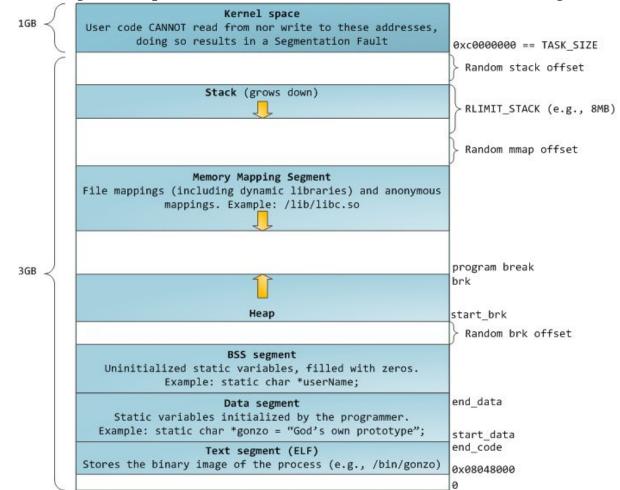
### Memory Map of Linux Process (32 bit)

Each process in a multi-tasking OS runs in its own memory sandbox.

This sandbox is the **virtual address space**, which in 32-bit mode is **always a 4GB block of memory addresses**.

These virtual addresses are mapped to physical memory by **page tables**, which are maintained by the operating system kernel and consulted by the processor.

### Memory Map of Linux Process (32 bit system)



https://manybutfinite.com/pos anatomy-of-a-program-in-me mory/

#### **NULL Pointer in C/C++**

```
int * pInt = NULL;
```

In possible definitions of NULL in C/C++:

```
#define NULL ((char *)0)
#define NULL 0
```

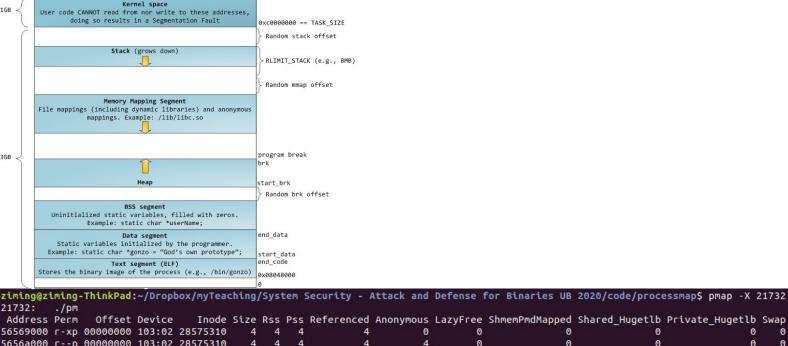
//since C++11
#define NULL nullptr

# /proc/pid\_of\_process/maps

Example processmap.c

#include <stdio.h> #include <stdlib.h></stdlib.h></stdio.h>								
int main() { getchar(); return 0; }								

cat /proc/pid/maps pmap -X pid pmap -X `pidof pm`



21732:	./pm																
Address	Регт	Offset	Device	Inode	Size	Rss	Pss	Referenced	Anonymous	LazyFree	ShmemPmdMapped	Shared_Hugetlb	Private_Hugetlb	Swap	SwapPss	Locke	l Mapping
56569000	г-хр	00000000	103:02	28575310	4	4	4	4	O	0	0	0	0	0	e	) (	) pm
5656a000	гр	00000000	103:02	28575310	4	4	4	4	4	0	0	O	0	0	e	) (	∂pm
5656b000	гw-р	00001000	103:02	28575310	4	4	4	4	4	0	0	0	0	0	e	) (	) pm
57cf2000	гw-р	00000000	00:00	0	136	4	4	4	4	0	0	0	0	0	e	) (	0 [heap]
f7d73000	г-хр	00000000	103:02	2883591	1876	772	772	772	0	0	0	0	0	0	e	) (	0 libc-2.27.so
f7f48000	p	001d5000	103:02	2883591	4	0	0	0	0	0	0	0	0	0	e	) (	0 libc-2.27.so
f7f49000	гр	001d5000	103:02	2883591	8	8	8	8	8	0	0	0	0	0	e	) (	) libc-2.27.so
f7f4b000	гw-р	001d7000	103:02	2883591	4	4	4	4	4	0	0	0	0	0	e	) (	0 libc-2.27.so
f7f4c000	гw-р	00000000	00:00	0	12	8	8	8	8	0	0	0	0	0	e	) (	
f7f75000	гw-р	00000000	00:00	0	8	8	8	8	8	0	0	0	0	0	6	) (	<b>)</b>
f7f77000	гр	00000000	00:00	Θ	12	0	0	O	O	0	0	0	0	0	6	) (	0 [vvar]
f7f7a000	г-хр	00000000	00:00	0	8	8	8	8	0	0	0	0	0	0	e	) (	0 [vdso]
f7f7c000	г-хр	00000000	103:02	2883587	152	144	144	144	O	0	0	0	0	0	e	) (	0 ld-2.27.so
f7fa2000	гр	00025000	103:02	2883587	4	4	4	4	4	0	0	0	0	0	e	) (	0 ld-2.27.so
f7fa3000	гw-р	00026000	103:02	2883587	4	4	4	4	4	0	0	Θ	0	0	e	) (	0 ld-2.27.so
ffef3000	гw-р	00000000	00:00	0	132	12	12	12	12	0	0	0	0	0	e	) (	0 [stack]
					====	===	===		========					====	=======	======	
					2372	988	988	988	60	0	0	0	0	0	e		Ð KB

### Memory Map of Linux Process (64 bit system)

ziming@ziming-Th <sup>.</sup>	inkPa	d:~/Dropbo	ox/myTea	aching/Sys	stem S	ecuri	.ty -	Attack and	d Defense	for Binari	es UB 2020/code	e/processmap\$ pr	nap -X 22891				
22891: ./pm64																	
Address	Perm	Offset	Device	Inode	Size	Rss	Pss	Referenced	Anonymous	LazyFree	ShmemPmdMapped	Shared_Hugetlb	Private_Hugetlb	Swap	SwapPss	Locked	Mapping
55bf7ae37000	г-хр	00000000	103:02	28577490	4	4	4	4	Θ	0	0	0	0	0	O	0	рмб4
55bf7b037000	гр	00000000	103:02	28577490	4	4	4	4	4	0	0	0	0	0	0	0	рмб4
55bf7b038000	гм-р	00001000	103:02	28577490	4	4	4	4	4	0	0	0	0	0	0	0	рмб4
55bf7cc0c000	гм-р	00000000	00:00	O	132	4	4	4	4	0	0	0	0	0	0	0	[heap]
7fc7ebdb6000	г-хр	00000000	103:02	660090	1948	992	5	992	Θ	0	0	Θ	0	0	0	0	libc-2.27.so
7fc7ebf9d000	p	001e7000	103:02	660090	2048	0	0	Ø	0	0	0	0	0	0	0	0	libc-2.27.so
7fc7ec19d000	гр	001e7000	103:02	660090	16	16	16	16	16	0	0	0	0	0	0	0	libc-2.27.so
7fc7ec1a1000	гw-р	001eb000	103:02	660090	8	8	8	8	8	0	0	0	0	0	0	0	libc-2.27.so
7fc7ec1a3000	гw-р	00000000	00:00	O	16	12	12	12	12	0	0	0	0	0	0	0	
7fc7ec1a7000	г-хр	00000000	103:02	660062	156	156	0	156	0	0	0	0	0	0	0	0	ld-2.27.so
7fc7ec3a6000	гм-р	00000000	00:00	O	8	8	8	8	8	0	0	0	0	0	0	0	
7fc7ec3ce000	гр	00027000	103:02	660062		4	4	4	4	0	0	0	0	0	O	0	ld-2.27.so
7fc7ec3cf000	гм-р	00028000	103:02	660062	4	4	4	4	4	0	0	0	0	0	O	0	ld-2.27.so
7fc7ec3d0000	гм-р	00000000	00:00	O	4	4	4	4	4	0	0	0	0	0	0	0	
7ffe05803000	гм-р	00000000	00:00	O	132	12	12	12	12	0	0	0	0	0	0	0	[stack]
7ffe058b9000	гр	00000000	00:00	O	12	0	0	0	0	0	0	0	0	0	0	0	[vvar]
7ffe058bc000	г-хр	00000000	00:00	O	8	4	0	4	Θ	0	0	0	0	0	0	0	[vdso]
fffffffff600000	г-хр	00000000	00:00	O	4	0	0	0	0	0	0	0	0	0	0	0	[vsyscall]
					====	====	===	=======			==============	==================	=============		=======	======	
					4512	1236	89	1236	80	0	0	0	0	0	0	0	KB